

Massively Parallel, Highly Efficient, but What About  
the Test Suite Quality?

## **Applying Mutation Testing to GPU Programs**

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# Example of GPU programming

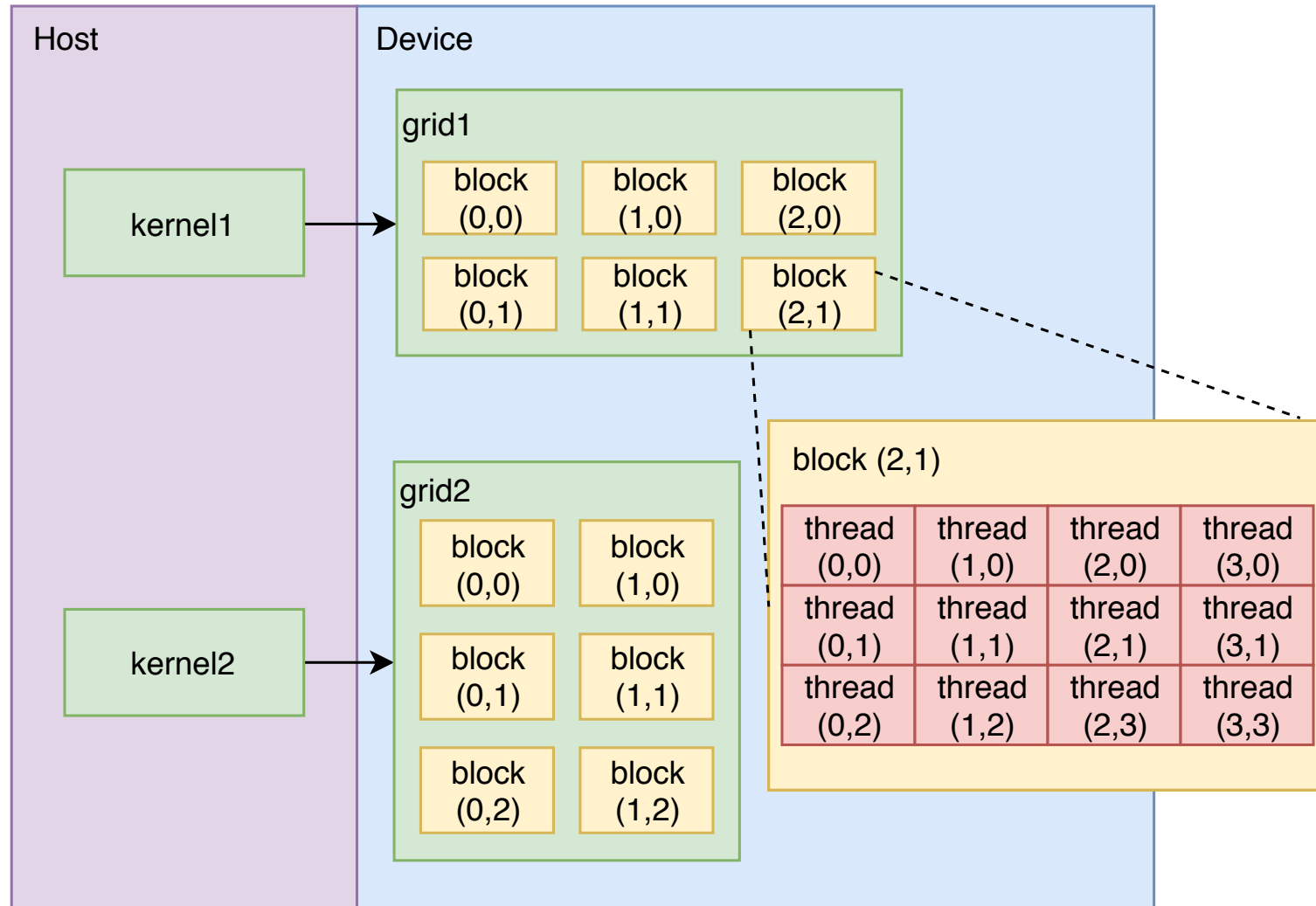
```
1 void sum(int n, float *a, float *b, float *c){
2   for (int i = 0; i < n; i++){
3     c[i] = a[i] + b[i];
4   }
5 }
```

*sum* function in Standard C

```
1 __global__ void sum(int n, float *a, float *b, float *c){
2   int i = blockIdx.x*blockDim.x + threadIdx.x;
3   if(i<n){
4     c[i] = a[i] + b[i];
5   }
6 }
```

*sum* function in CUDA C

# CUDA programming model



# Example of GPU programming

```
1 __global__ void sum(int n, float *a, float *b, float *c){
2   int i = blockIdx.x*blockDim.x + threadIdx.x;
3   if(i<n){
4     c[i] = a[i] + b[i];
5   }
6 }
```

Simple  
Program  
Multiple  
Data



Block 0

Thread 0

```
__global__ void sum(...){
  int i = 0;

  if (i < n){
    c[i] = a[i] + b[i];}}}
```

Thread 1

```
__global__ void sum(...){
  int i = 1;

  if (i < n){
    c[i] = a[i] + b[i];}}}
```

...

Thread 2

```
__global__ void sum(...){
  int i = 2;

  if (i < n){
    c[i] = a[i] + b[i];}}}
```

Thread 3

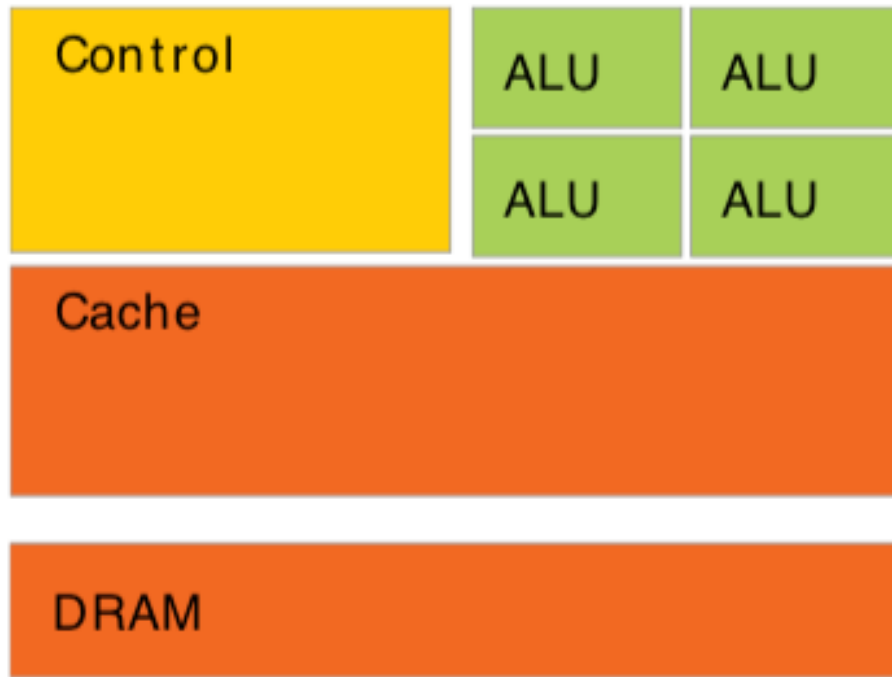
```
__global__ void sum(...){
  int i = 3;

  if (i < n){
    c[i] = a[i] + b[i];}}}
```

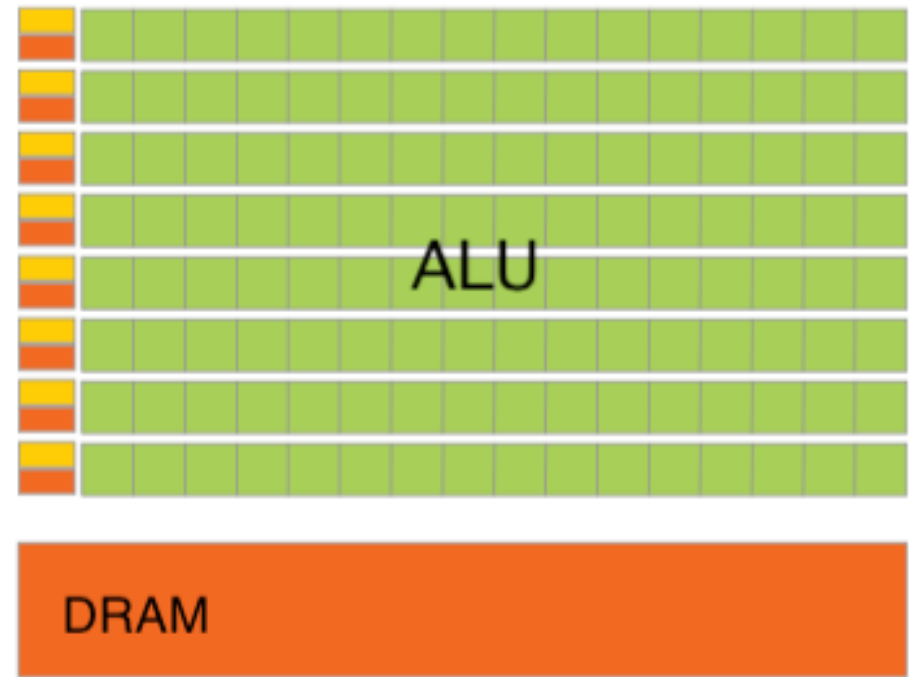
...

Actual code in CUDA parallel threads

# Comparison of CPU and GPU



CPU



GPU

GPU contains many more transistors devoted to data processing rather than data caching and flow control

# Challenges in GPU programming

- *Thread management*

```
int tid = blockIdx.x  
        + threadIdx.x * blockDim.x;
```

Indexing Bugs

# Challenges in GPU programming

- *Memory management*

```
__shared__ float cache[threadsPerBlock];
int i = blockDim.x/2;
while (i != 0) {
    if (caIndex < i)
        cache[caIndex] += cache[caIndex + i];
    __syncthreads();
    i /= 2;
}
```

Shared Memory Bugs

# Challenges in GPU programming

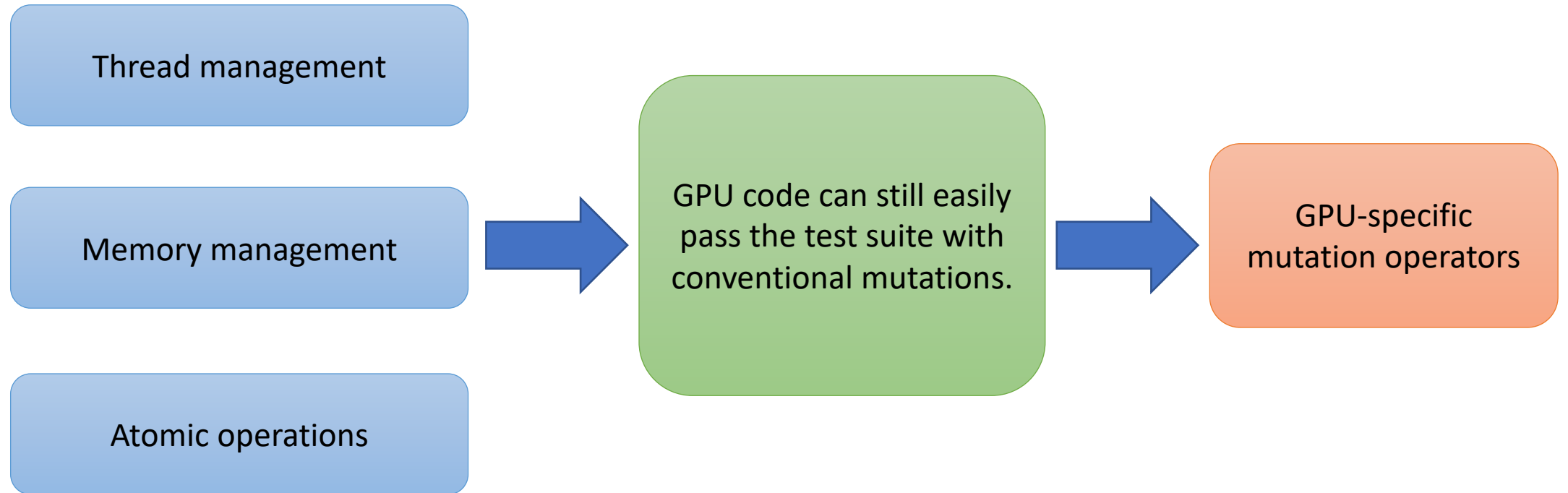
- *Atomic operations*

```
__global__ void histo_kernel(  
    unsigned char *buffer,  
    long size,  
    unsigned int *histo ){  
int i = threadIdx.x +  
    blockIdx.x * blockDim.x;  
int stride = blockDim.x * gridDim.x;  
while (i < size) {  
    histo[buffer[i]] += 1;  
    i += stride;  
}  
}
```

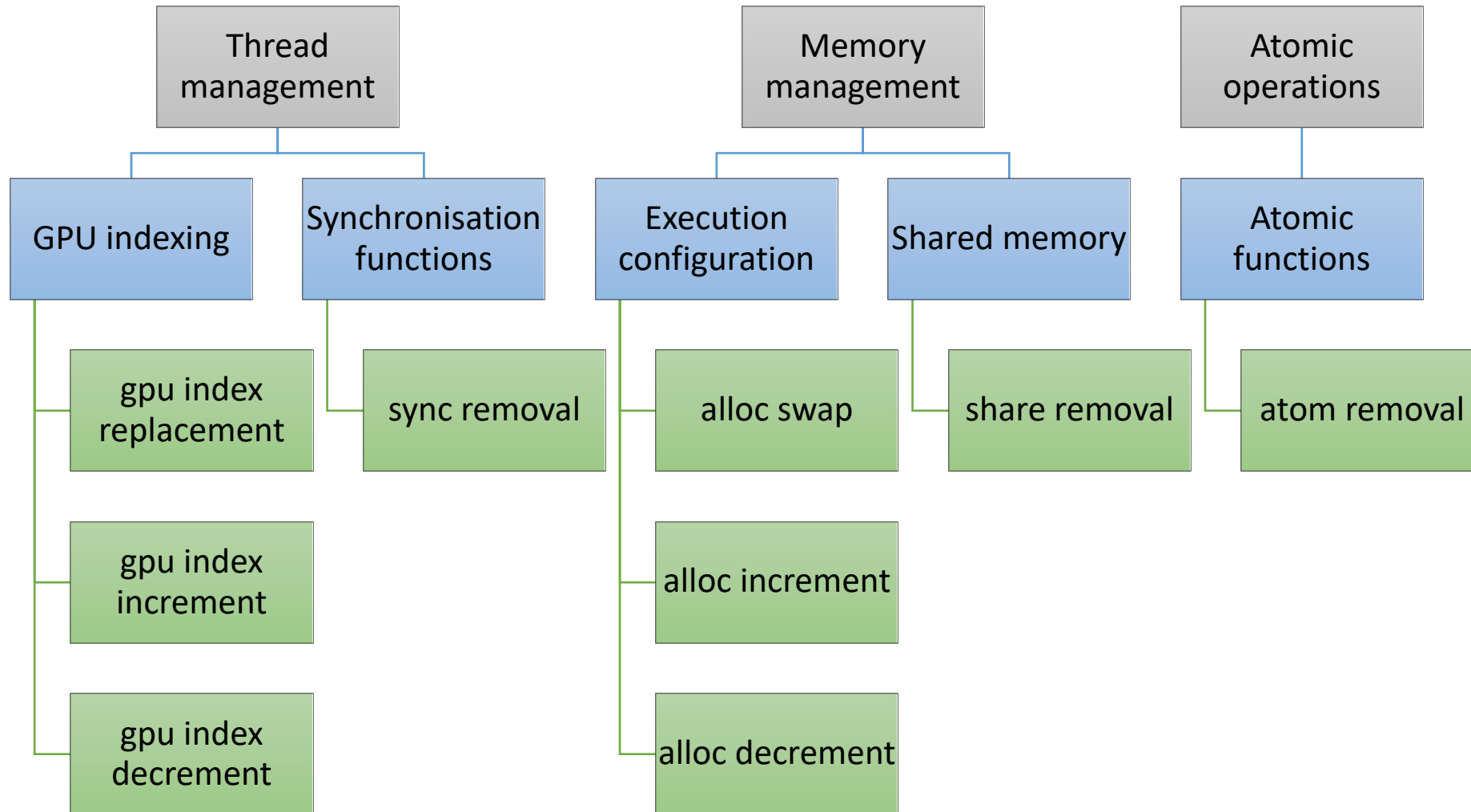
Atomic Operation Omissions



# Challenges in GPU programming



# GPU-specific mutation operators



# Experimental study

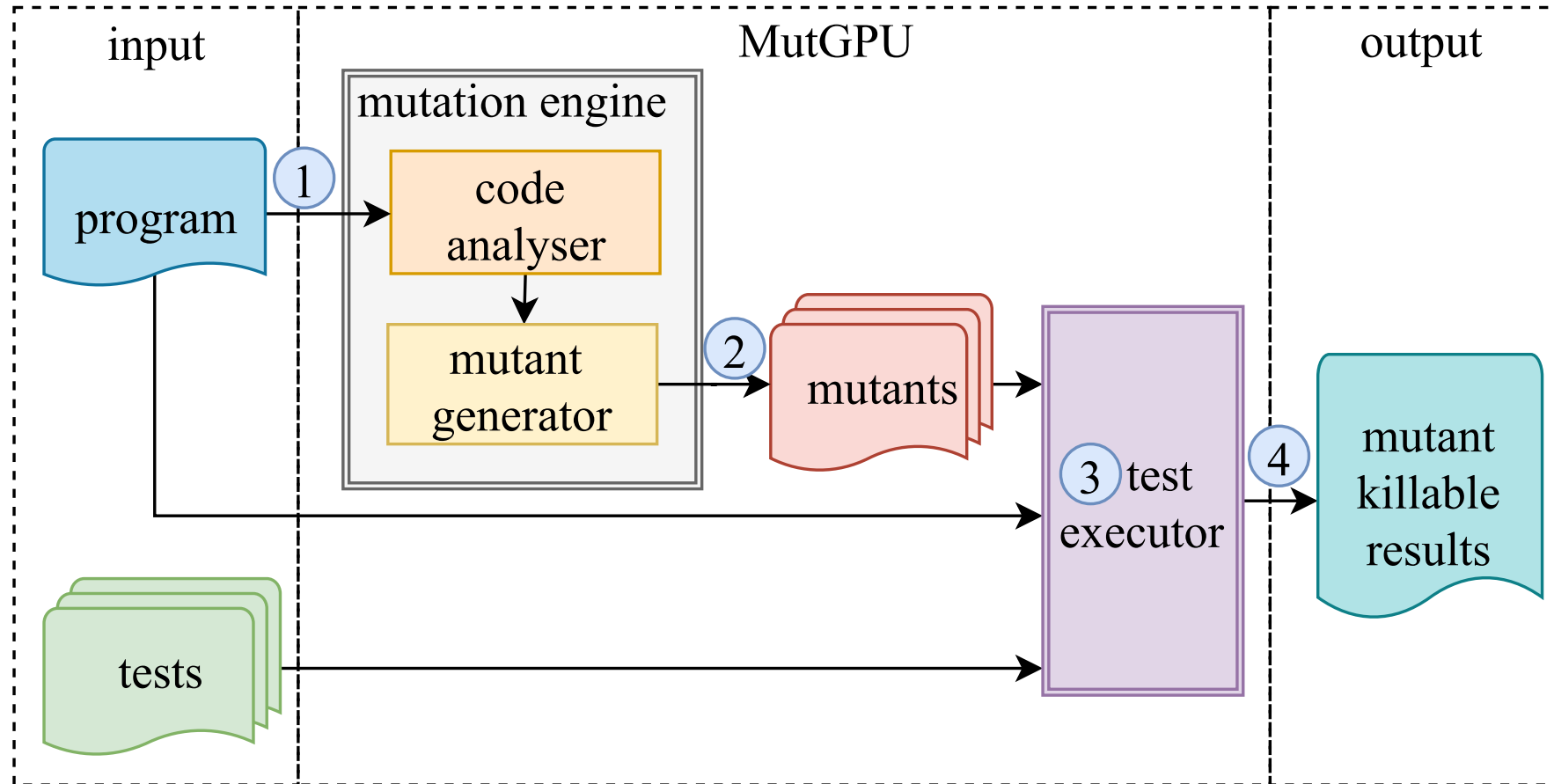
- Subjects



- 6 GPU benchmark projects from CUDA SDK
- 2 NVIDIA graphic cards
  - GeForce MX150 & GTX 960
- 2 releases of CUDA toolkit (9.0 & 9.1)

# Experimental study

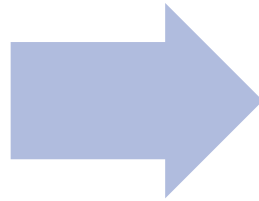
- Tool



# Experimental setup

## Initial evaluation

- #equivalent mutants
- #generated mutants
- mutation scores



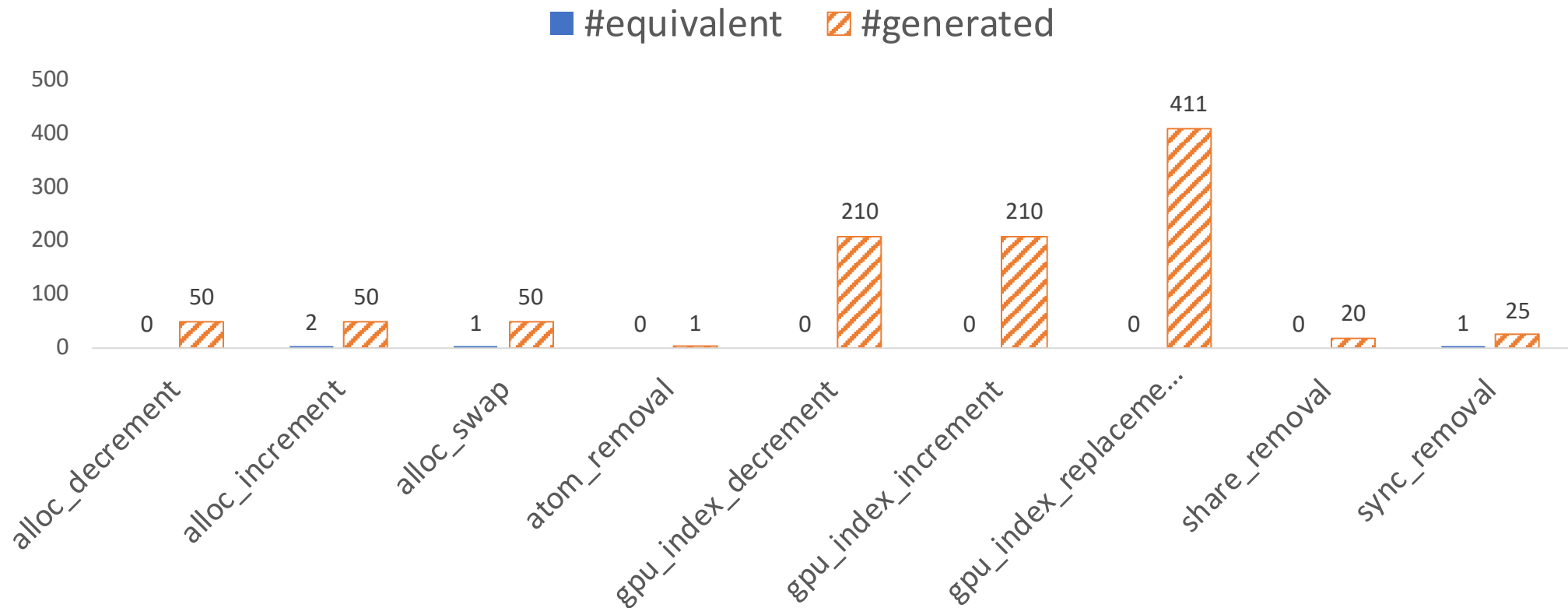
## Conventional vs. GPU-specific

- Effectiveness
- C-sufficient test suite

# Results

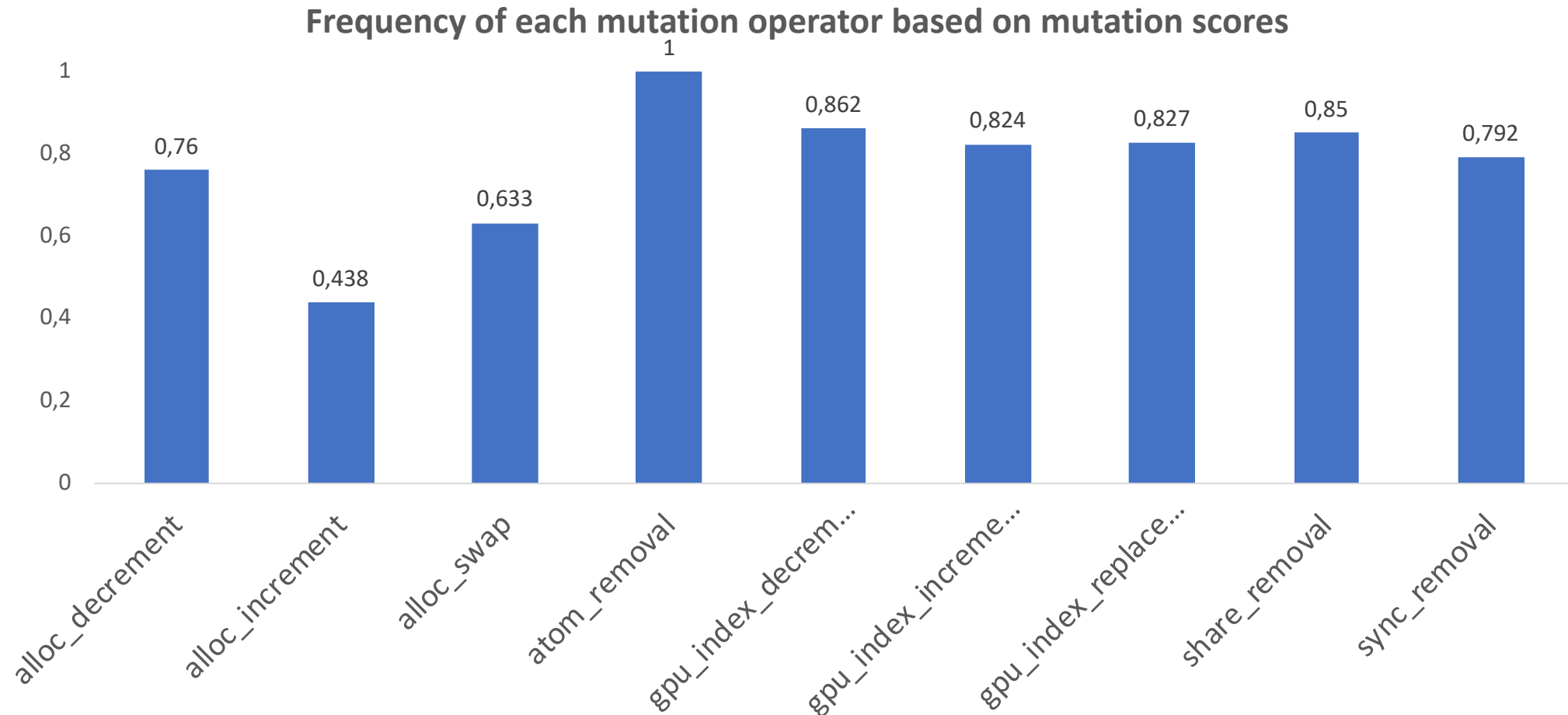
- Initial evaluation of GPU-specific mutation operators

Frequency of each mutation operator based on #equivalent and #generated



# Results

- Initial evaluation of GPU-specific mutation operators



# Results

- Conventional vs. GPU-specific
  - Manual analysis of surviving non-equivalent mutants

## Conventional

- Guide to write direct tests
- Effort to kill a mutant is within 1 min

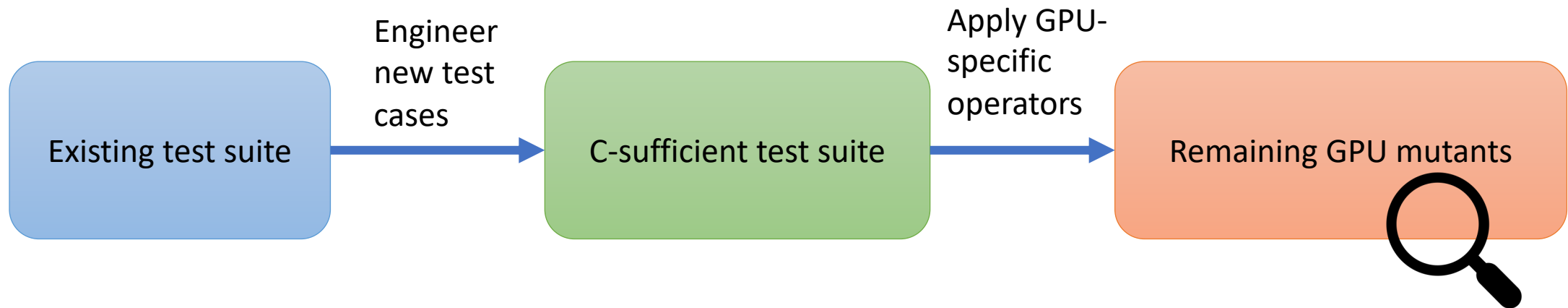
## GPU-specific

- Evaluate the test quality in the context of GPU programming
- Effort to kill a mutant is up to hours



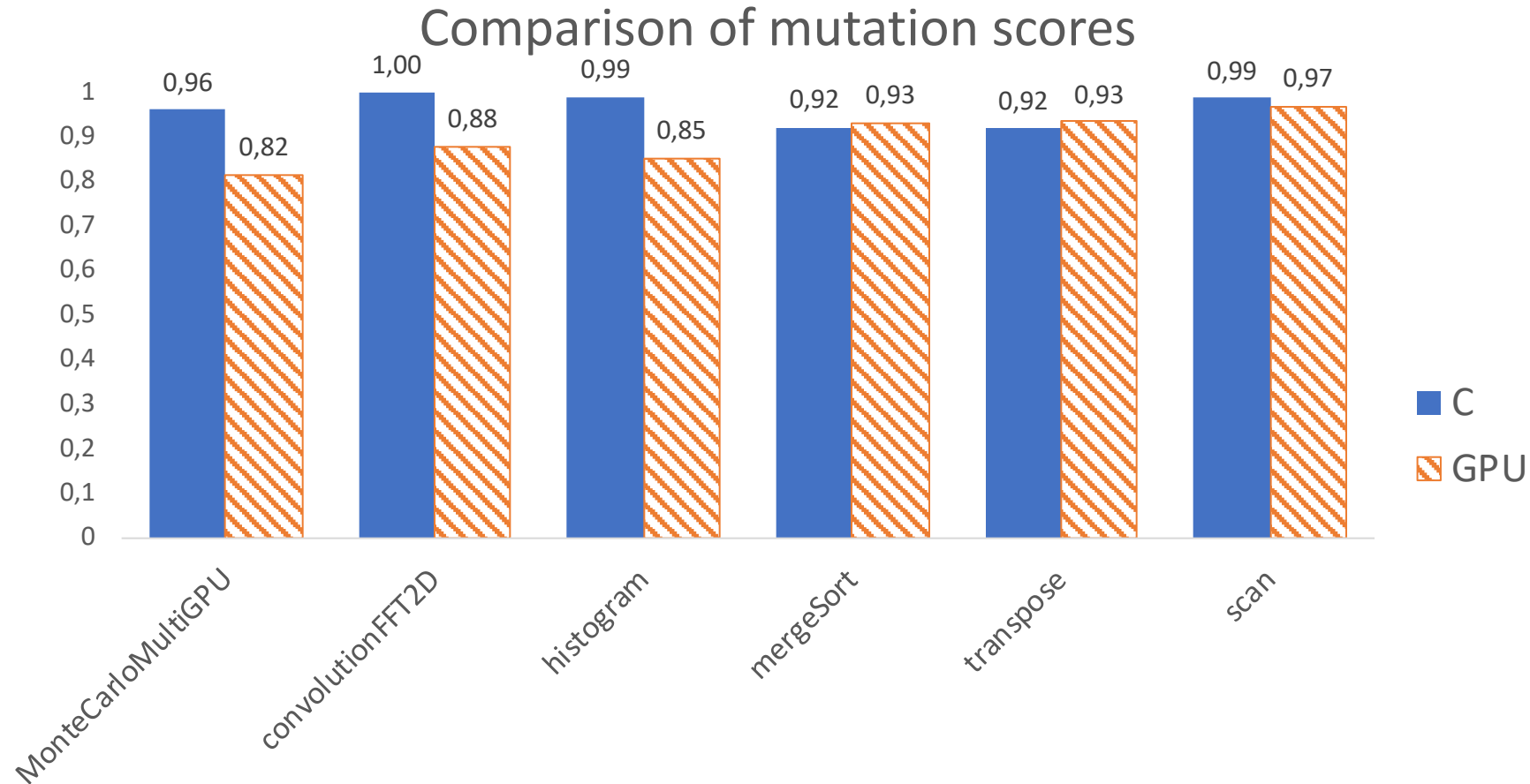
# Results

- Conventional vs. GPU-specific
  - C-sufficient test suite



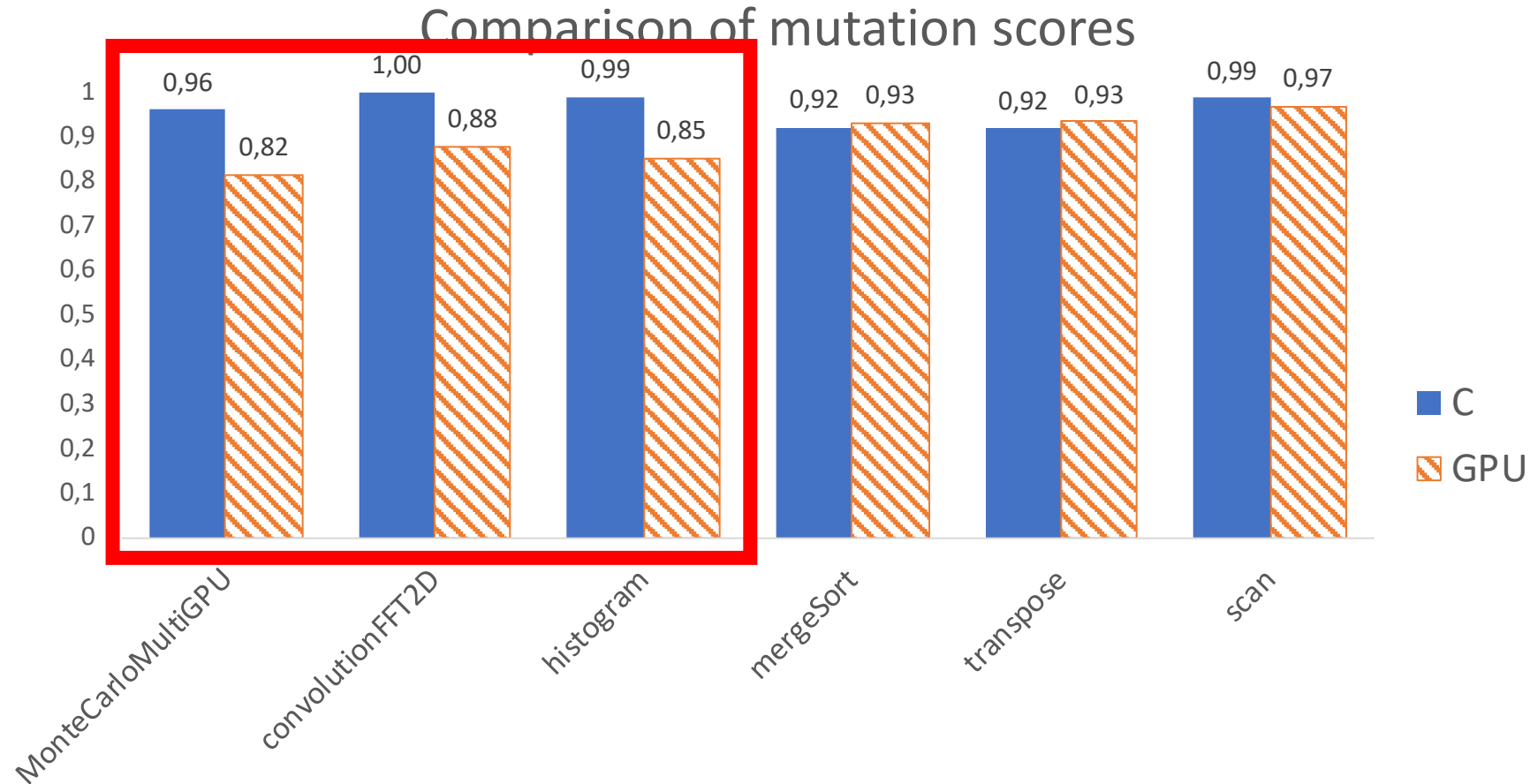
# Results

- Conventional vs. GPU-specific
  - C-sufficient test suite



# Results

- Conventional vs. GPU-specific
  - C-sufficient test suite



# Summary

- What we have done:
  - 9 GPU-specific mutation operators
  - Comparison of conventional and GPU-specific mutation operators
  - A preliminary experiment on 6 GPU applications
- What we have learned:
  - Conventional operators: simple direct tests
  - GPU-specific operators: more delicate test cases (thus higher quality and more test effort)
  - equivalent mutants: bug detection